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Patent/TM/App/Serial # 10/645,529

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Docket No. 241014US2S DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE -2 M 9:30

IN RE PATENT OF: Koichi KOKUBUN

PATENT NO. 6,858,491

SERIAL NO. 10/645,529

ISSUE DATE: February 22, 2005

FOR: SEMICONDUCTOR DEVICE HAVING TRENCH CAPACITOR FORMED IN SOI
SUBSTRATE, AND METHOD OF MANUFACTURING THE SAME

REQUEST FOR REFUND OF PATENT PUBLICATION FEE

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The above patent issued on February 22, 2005. The Patent Application Publication has either not been published as of this date, or was published more than thirty days from the date the patent issued. Accordingly, the \$300.00 fee should be refunded.

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Respectfully Submitted,

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MAIER & NEUSTADT, P.C.

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(OSMMN 03/05)

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Application Number Information

Application Number: 10/645529

Examiner Number: 77022 / PHAM, HOAIAssignments

Filing or 371(c) Date: 08/22/2003

Group Art Unit: 2814

IFW IMAGE

Effective Date: 08/22/2003

Class/Subclass: 438/243.000

Application Received: 08/22/2003

Lost Case: NO

Patent Number: 6858491

Interference Number:

Issue Date: 02/22/2005

Unmatched Petition: NO

Date of Abandonment: 00/00/0000

L&R Code: Secrecy Code:1

Attorney Docket Number: 241014US2S DIV Third Level Review: NO

Secrecy Order: NO

Status: 150 /PATENTED CASE

Status Date: 02/02/2005

Confirmation Number: 8104

Oral Hearing: NO

Title of Invention: METHOD OF MANUFACTURING THE SEMICONDUCTOR DEVICE
HAVING A CAPACITOR FORMED IN SOI SUBSTRATE

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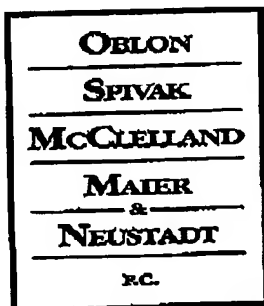
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NO. 300 P. 1



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